


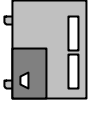
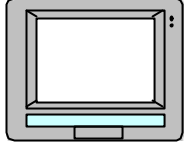


F. PMU Communication With Mitsubishi PLC CPU(Loader Port)

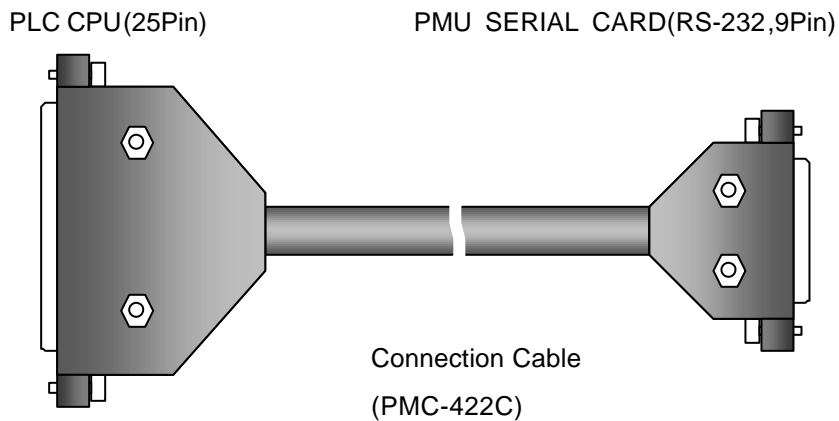
This is for communicating between PMU _ Mitsubishi PLC Loader Port

1-1 System Configuration

PLC	Communication Unit	Cable	Communication Module for PMU	PMU
				
M2N, M3N	None	PMC-422C	PMO-600S	PMU-600
M2A, M3A			PMO-300S	PMU-300
M2U, M3U			PMO-200S	PMU-200
A1S, A2S				
MOJ2, A0J2H				
QnA				
FX				

1-2 Cable Connection

(1)RS-232C Connection (Melsec Series PMU)



Appendix. Getting Started

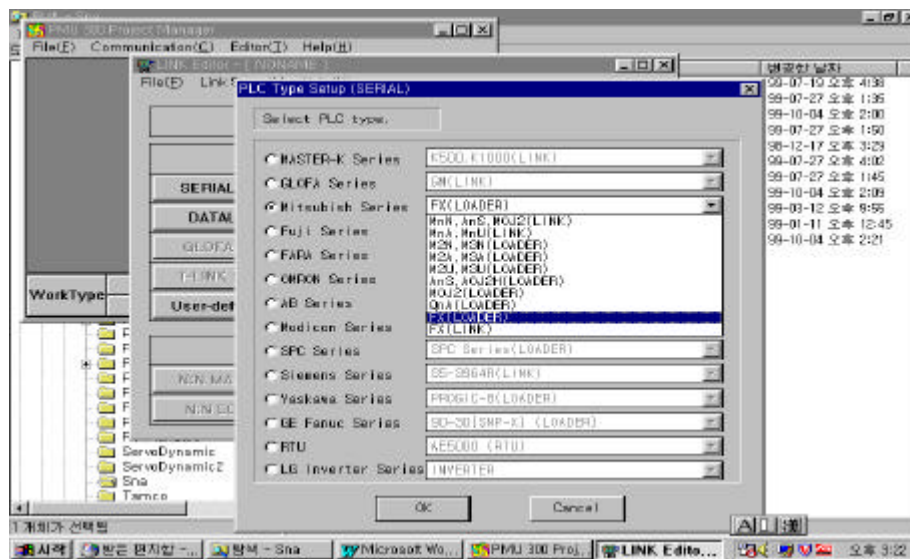
1-3 Melsec PLC Set up

You don't need to set any data for Melsec PLC.

1-4 PMU Set up

(1) Link Set up

Please select Interface Type(" Serial") first and then select PLC Type as follows;(" A2N,A3N(LOADER)" , " A2A,A3A(LOADER)" , " A2U,A3U(LOADER)" , " AnS,AOJ2H(LOADER)" , " A0J2(LOADER)" , " QnA(LOADER)" , " FX")



Please set up buffer memory which will be linked with PLC address.

Please refer to Table for Available PLC Address.

Transfer Information(Link) to PMU.

(2) serial Set up

You don't need to set up anything, Data will be set up automatically as follows.

Baud Rate : 9600 bps

Data Length : 8bit

Stop Bit Length : 1bit

Parity Bit : Even

Signal Level : RS-232C

Station No. : 0

Table 2-1 Available PLC Address Table

(1) AnU CPU (Loader Port Communication)

Contents	Dev.No	Data	Registered Bit Address	Registered Word Address
Input (X)	0	Bit	X0000 – X1FFF	
Output (Y)	1	Bit	Y0000 – Y1FFF	
LINK Relay (B)	2	Bit	B0000 – B1FFF	
LINK Register (W)	3	Word		W0000 – W1FFF
STEP Relay (S)	4	Bit	S0000 - S8191	
Special Relay (F)	5	Bit	F0000 – F2047	
LATCH Relay (L)	6	Bit	L0000 – L8191	
Internal Relay (M)	7	Bit	M0000 – M8191	
Special Relay (M)	7	Bit	M9000 - M9255	
Data Register (D)	8	Word		D0000 - D8191
Special Register (D)	8	Word		D9000 - D9255
Timer-Coli (TC)	9	Bit	TC000 – TC2047	
Timer-Current (TN)	10	Word		TN000 – TN2047
Timer-Contact (TS)	11	Bit	TS000 – TS2047	
Counter-Coil (CC)	12	Bit	CC000 – CC1023	
Counter-Current (CN)	13	Word		CN000 – CN1023
Counter-Contact (CS)	14	Bit	CS000 – CS1023	

Appendix. Getting Started

(2) QnA CPU (Loader Port Communication)

Contents	Dev.No	Data	Registered Bit Address	Registered Word Address
Input (X)	0	Bit	X0000 – X1FFF	
Output (Y)	1	Bit	Y0000 – Y1FFF	
Link Relay (B)	2	Bit	B0000 – B7FFF	
Link Register (W)	3	Word		W0000 – W63FF
Special Link Register (SW)	3	Word		SW000 – SW7FF
Step Relay (S)	4	Bit	S00000 - S32767	
Special Relay (F)	5	Bit	F00000 – F32767	
Latch Relay (L)	6	Bit	L00000 – L32767	
Edge Relay (V)	-	Bit	V00000 - V32767	
Internal Relay (M)	7	Bit	M00000 – M32767	
Special Relay (SM)	7	Bit	SM0000 – SM2047	
Data Register (D)	8	Word		D00000 – D25599
Special Register (SD)	8	Word		SD0000 – SD2047
Timer-Coil (TC)	9	Bit	TC0000 – TC23551	
Timer-Current (TN)	10	Word		TN0000 – TN23551
Timer-Contact (TS)	11	Bit	TS0000 – TS23551	
Accumulating Timer-Coil (SC)	-	Bit	SC0000 – SC23551	
Accumulating Timer-Current (SN)	-	Word		SN0000 – SN23551
Accumulating Timer-Contact (SS)	-	Bit	SS0000 – SS23551	
Counter-Coil (CC)	12	Bit	CC0000 – CC23551	
Counter-Current (CN)	13	Word		CN0000 – CN23551
Counter-Contact (CS)	14	Bit	CS0000 – CS23551	

(3) MELSEC-FX Series CPU (Loader Port Communication)

Contents	Dev.No	Data	Registered Bit Address	Registered Word Address
Input (X)	0	Bit	X000 – X337	X000 – X320
Output (Y)	1	Bit	Y000 – Y337	Y000 – Y320
Step Relay (S)	2	Bit	S000 – S999	S000 – S984
Internal Relay (M)	3	Bit	M0000 – M1535	M0000 – M1520
Data Register (D)	4	Word		D000 – D999
Timer- (TN)	5	Word		TN000 – TN255
Counter-Current (CN)	6	Word		CN000 - CN255